

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1 - 21. Canceled.

22. (New) A plasma display panel comprising:
a plurality of first sustain electrodes arranged on a substrate; and
a plurality of second sustain electrodes spaced apart from the first sustain electrodes to form a plurality of gaps, wherein the first and the second sustain electrodes form a plurality of electrode pairs, wherein a total surface area of the plurality of gaps is less than or equal to about 25% of a total surface area of the first sustain electrodes and the second sustain electrodes.

23. (New) The plasma display panel of claim 23, wherein the first sustain electrodes and the second sustain electrodes are alternately arranged on the substrate.

24. (New) The plasma display panel of claim 23, wherein adjacent pairs are in different positions with respect to the first and second sustain electrodes.

25. (New) The plasma display panel of claim 23, further comprising:
a plurality of barriers including at least one of a plurality of first barriers formed perpendicular to the first sustain electrodes and a plurality of second barriers formed parallel to the first sustain electrodes; and
a dielectric layer formed over the plurality of first sustain electrodes and the plurality of second sustain electrodes.
26. (New) The plasma display panel of claim 23, wherein the dielectric layer is formed to cover the plurality of first and second sustain electrodes and has a thickness of about $25\mu\text{m}$ or more.
27. (New) The plasma display panel of claim 23, wherein a total surface area of the first sustain electrodes is substantially equal to a total surface area of the second sustain electrodes.
28. (New) The plasma display panel of claim 23, wherein the first sustain electrodes and the second sustain electrodes comprise a transparent electrode having a predetermined width, and a metal electrode having a smaller width than the transparent electrode to partially overlap the transparent electrode.

29. (New) A plasma display panel comprising:
- a plurality of first sustain electrodes arranged on a substrate; and
 - a plurality of second sustain electrodes spaced apart from the first sustain electrodes to form a plurality of gaps, the first and second sustain electrodes form a plurality of electrode pairs, wherein a combined width of the plurality of gaps is less than or equal to about 25% of a combined width of the first sustain electrodes and the second sustain electrodes.
30. (New) The plasma display panel of claim 29, wherein the first sustain electrodes and the second sustain electrodes are alternately arranged on the substrate.
31. (New) The plasma display panel of claim 29, wherein the first sustain electrodes and the second sustain electrodes are arranged in pairs where adjacent pairs are in different positions with respect to the first and second sustain electrodes.
32. (New) The plasma display panel of claim 29, further comprising:
- a plurality of barriers including at least one of a plurality of first barriers formed perpendicular to the first sustain electrodes and a plurality of second barriers formed parallel to the first sustain electrodes; and
 - a dielectric layer formed over the plurality of first sustain electrodes and the plurality of second sustain electrodes.

33. (New) The plasma display panel of claim 29, wherein the dielectric layer is formed to cover the plurality of first and second sustain electrodes, and has a thickness of about 25 μm or more.

34. (New) The plasma display panel of claim 29, wherein a combined width of the first sustain electrodes is substantially equal to a combined width of the second sustain electrodes.

35. (New) The plasma display panel of claim 29, wherein widths of at least two of the plurality of gaps are substantially equal or different.

36. (New) The plasma display panel of claim 29, wherein the first sustain electrodes and the second sustain electrodes comprise a transparent electrode having a predetermined width, and a metal electrode having a smaller width than the transparent electrode to partially overlap the transparent electrode.

37. (New) A plasma display panel comprising:
a plurality of first sustain electrodes arranged on a substrate; and
a plurality of second sustain electrodes spaced apart from the first sustain electrodes to form a plurality of gaps, the first and second sustain electrodes forming a plurality

of electrode pairs, wherein a combined width of the plurality of gaps is less than or equal to about 50% of a combined width of the first sustain electrodes.

38. (New) The plasma display panel of claim 37, wherein the first sustain electrodes and the second sustain electrodes are alternately arranged on the substrate.

39. (New) The plasma display panel of claim 37, wherein the first sustain electrodes and the second sustain electrodes are arranged in pairs where adjacent pairs are in different positions with respect to the first and second sustain electrodes.

40. (New) The plasma display panel of claim 37, further comprising:
a plurality of barriers including at least one of a plurality of first barriers formed perpendicular to the first sustain electrodes and a plurality of second barriers formed parallel to the first sustain electrodes; and
a dielectric layer formed over the plurality of first sustain electrodes and the plurality of second sustain electrodes.

41. (New) The plasma display panel of claim 37, wherein the dielectric layer is formed to cover the plurality of first and second sustain electrodes and has a thickness of about 25 μm or more.

42. (New) The plasma display panel of claim 37, wherein a combined width of the first sustain electrodes is substantially equal to a combined width of the second sustain electrodes.

43. (New) The plasma display panel of claim 37, wherein widths of at least two of the plurality of gaps are substantially equal.

44. (New) The plasma display panel of claim 37, wherein the first sustain electrodes and the second sustain electrodes comprise a transparent electrode having a predetermined width, and a metal electrode having a smaller width than the transparent electrode to partially overlap the transparent electrode.

45. (New) A plasma display panel comprising:
a plurality of first sustain electrodes arranged on a substrate; and
a plurality of second sustain electrodes spaced apart from the first sustain electrodes to form a plurality of gaps, the first and second sustain electrodes forming a plurality of electrode pairs, wherein each gap has a width being less than or equal to about 20% of a pixel pitch, which is an overall distance of three adjacent display cells, wherein the three adjacent display cells are a red display cell, a green display cell, and a blue display cell.

46. (New) The plasma display panel of claim 45, wherein the first sustain electrodes and the second sustain electrodes are alternately arranged on the substrate.

47. (New) The plasma display panel of claim 45, wherein the first sustain electrodes and the second sustain electrodes are arranged in pairs where adjacent pairs are in different positions with respect to the first and second sustain electrodes.

48. (New) The plasma display panel of claim 45, further comprising:
a plurality of barriers including at least one of a plurality of first barriers formed perpendicular to the first sustain electrodes and a plurality of second barriers formed parallel to the first sustain electrodes; and

a dielectric layer formed over the plurality of first sustain electrodes and the plurality of second sustain electrodes.

49. (New) The plasma display panel of claim 45, wherein the dielectric layer is formed to cover the plurality of first and second sustain electrodes, and has a thickness of about 25 μm or more.

50. (New) The plasma display panel of claim 45, wherein a combined width of the first sustain electrodes is substantially equal to a combined width of the second sustain electrodes.

51. (New) The plasma display panel of claim 45, wherein widths of at least two of the plurality of gaps are substantially equal.

52. (New) The plasma display panel of claim 45, wherein the first sustain electrodes and the second sustain electrodes comprise a transparent electrode having a predetermined width, and a metal electrode having a smaller width than the transparent electrode to partially overlap the transparent electrode.

53. (New) The plasma display panel of claim 45, wherein the pixel pitch is an overall distance of three adjacent display cells including a width of barrier between the display cells.

54. (New) A plasma display panel comprising:
a plurality of first sustain electrodes arranged on a substrate; and
a plurality of second sustain electrodes spaced apart from the first sustain electrodes to form a plurality of gaps, the first and second sustain electrodes forming a plurality of electrode pairs, wherein a combined width of the plurality of gaps is less than or equal to

about 25% of a combined width of the first sustain electrodes and the second sustain electrodes, and each gap has a width being less than or equal to about 20% of a pixel pitch, which is an overall distance of three adjacent display cells, wherein the three adjacent display cells are a red display cell, a green display cell, and a blue display cell.

55. (New) The plasma display panel of claim 54, wherein the first sustain electrodes and the second sustain electrodes are alternately arranged on the substrate.

56. (New) The plasma display panel of claim 54, wherein the first sustain electrodes and the second sustain electrodes are arranged in pairs where adjacent pairs are in different positions with respect to the first and second sustain electrodes.

57. (New) The plasma display panel of claim 54, further comprising:
a plurality of barriers including at least one of a plurality of first barriers formed perpendicular to the first sustain electrodes and a plurality of second barriers formed parallel to the first sustain electrodes; and
a dielectric layer formed over the plurality of first sustain electrodes and the plurality of second sustain electrodes.

58. (New) The plasma display panel of claim 54, wherein the dielectric layer is formed to cover the plurality of first and second sustain electrodes, and has a thickness of about $25\mu\text{m}$ or more.

59. (New) The plasma display panel of claim 54, wherein a combined width of the first sustain electrodes is substantially equal to a combined width of the second sustain electrodes.

60. (New) The plasma display panel of claim 54, wherein widths of at least two of the plurality of gaps are substantially equal.

61. (New) The plasma display panel of claim 54, wherein the first sustain electrodes and the second sustain electrodes comprise a transparent electrode having a predetermined width, and a metal electrode having a smaller width than the transparent electrode to partially overlap the transparent electrode.

62. (New) The plasma display panel of claim 54, wherein the pixel pitch is an overall distance of three adjacent display cells including a width of barrier between the display cells.

63. (New) The plasma display panel of claim 54, wherein a combined width of the plurality of gaps is less than or equal to about 50% of a combined width of the first sustain electrodes.

64. (New) The plasma display panel of claim 54, wherein a combined width of the plurality of gaps is less than or equal to about 50% of a combined width of the second sustain electrodes.

65. (New) A plasma display panel comprising:
a first sustain electrode arranged on a substrate; and
a second sustain electrode spaced apart from the first sustain electrode to form a gap, wherein a width of the gap is less than or equal to about 25% of a combined width of the first sustain electrode and the second sustain electrode, and the gap has a width being less than or equal to about 20% of a pixel pitch, which is an overall distance of three adjacent display cells, wherein the three adjacent display cells are a red display cell, a green display cell, and a blue display cell.

66. (New) The plasma display panel of claim 65, further comprising:

a plurality of barriers including at least one of a plurality of first barriers formed perpendicular to the first sustain electrode and a plurality of second barriers formed parallel to the first sustain electrode; and

a dielectric layer formed over the first sustain electrode and the second sustain electrode.

67. (New) The plasma display panel of claim 65, wherein the dielectric layer is formed to cover the plurality of first and second sustain electrodes, and has a thickness of about $25\mu\text{m}$ or more.

68. (New) The plasma display panel of claim 65, wherein a width of the first sustain electrode is substantially equal to a width of the second sustain electrode.

69. (New) The plasma display panel of claim 65, wherein the first sustain electrode and the second sustain electrode comprise a transparent electrode having a predetermined width, and a metal electrode having a smaller width than the transparent electrode to partially overlap the transparent electrode.

70. (New) The plasma display panel of claim 65, wherein the pixel pitch is an overall distance of three adjacent display cells including a width of barrier between the display cells.

71. (New) The plasma display panel of claim 65, wherein the width of the gap is less than or equal to about 50% of the width of the first sustain electrode.

72. (New) The plasma display panel of claim 65, wherein the width of the gap is less than or equal to about 50% of the width of the second sustain electrode.